Please amend the Specification as follows:

1) In the application as filed (*i.e.,* the copy of the International application), change the paragraph that begins on page 3 (referring to the Arabic numerals at the top of each page), line 17 to read as follows:

A particularly advantageous technique for controlling the selection of the supply voltage, and adjustments thereto, to improve efficiency is taught in British patent application **publication** number ~~**0303826.2**~~ **2398648**.

2) In the application as filed, change the two paragraphs that begin on page 4, line 3 to read as follows:

[[Te]] **The** first voltage may be greater than the second voltage and the summed voltage is provided on the second tap of the primary side of the transformer.

The first voltage may be a variable voltage. The first voltage may be provided by a **first** switchable voltage source. The second voltage may be variable. The second voltage may be provided by a **second** switchable voltage source.

3) In the application as filed, change the paragraph that begins on page 4, line 11 to read as follows:

The first voltage ~~**signal**~~ may be a coarse voltage signal and the second voltage ~~**signal**~~ may be a fine voltage signal. The fine voltage signal may be representative of an error in the course voltage signal.

4) In the application as filed, change the four paragraphs that begin on page 5, line 1 to read as follows:

~~**A method according to claim 15, wherein the first voltage is**~~ **The first voltage is preferably** applied between the first tap of the primary side and the second tap of the primary side, and the second voltage is **preferably** applied to a first tap of the secondary side, wherein a summed voltage is provided on a second tap of the primary or secondary side.

The first voltage may be greater than the second voltage and the summed voltage may be provided on the second tap of the primary side of the transformer.

~~**The method according to any one of claims 15 to 17 further comprising**~~ **The method may further comprise** the step of varying the first voltage. The method may further comprise the step of varying the second voltage. The method may comprise varying the first voltage between n levels and varying the second voltage between m levels, wherein the summed voltage is thereby variable between n*m levels.

The first voltage ~~**signal**~~ may be a coarse voltage signal and the second voltage ~~**signal**~~ may be a fine voltage signal. The fine voltage signal may be representative of an error in the course voltage signal.

5) In the application as filed, change the paragraph that begins on page 8, line 25 to read as follows:

The fine correction voltage source 204**, which in the embodiment of Figure 2 is a fixed voltage source,** provides an alternating voltage signal on an output line 226, to correct errors in the voltage signal provided on the output line 224 of the switchable main voltage source 202. The correction voltage generated on line 226 by the fine correction voltage source 204 is summed with the selected output voltage on line 224 to correct or minimise any error therein.

6) In the application as filed, change the paragraph that begins on page 18, line 27 to read as follows:

In the example of utilising a transformer having multiple primary windings in order to sum multiple signals, the primary ~~**sigmnals**~~ **signals** are all summed together with the voltage that is applied to the input side of the secondary winding.